

## ESD Protection Devices

Part Number	Package	Peak Power Dissipation	Reverse Standoff Voltage	Maximum Reverse Leakage	Breakdown Voltage	Maximum Clamping Voltage	Maximum Peak Pulse Current	MAX C <sub>J</sub>	Marking Code	Internal Diagram
		P <sub>PK</sub> (W)	V <sub>RWM</sub> (V)	I <sub>R</sub> (uA)	V <sub>BR</sub> (V)	V <sub>C</sub> (V)	I <sub>PP</sub> (A)	C <sub>J</sub> (pF)		
ESDBL5V0AE1	0201	62.5	5	0.1	8	12.5	5	10	H	Fig.1
ESDBU5V0AE1	0201	80	5	1	6.5	20	4	0.9	X	Fig.1
ESDBV5V0AE1	0201	52	5	0.1	8	13	4	2	I	Fig.1
ESDU5V0AE1	0201	80	5	1	5.8	20	4	0.9	AE	Fig.2
ESDLC18VAE1	0201	120	18	1	24	40	3.3	0.5	-	Fig.1
ESDB5V0AE2	0201-A	84	5	0.1	6	12	7	25	Z4	Fig.1
ESDLC5V0AE2	0201-A	125	5	0.1	6	25	5	0.32	ZZ	Fig.1
ESDLC5V0AE3	0201-C	30	5	0.1	10	12	2	3	-	Fig.1
ESDLC0524DFN10	DFN-10	150	5	0.9	6.1	18	5	0.8	-	Fig.5
ESDLC5V0DFN10	DFN-10	150	5	1	6	28	5	0.5	P524/0524P	Fig.5
ESDLC0524DFN10	DFN-10	-	5	0.9	6	15	5	0.8	-	Fig.5
ESD3V3L	DFN1006-2	300	3.3	1	4.2	15	20	250	3F	Fig.2
ESDL3V3LB	DFN1006-2	50	3.3	0.05	3.5	8	6	10	-	Fig.1
ESDLC3V3L	DFN1006-2	150	3.3	0.5	3.5	15	10	20	3P	Fig.2
ESDLC3V3LB	DFN1006-2	-	3.3	0.1	6.8	11	1	3.1	3X	Fig.1
ESDSC3V3LB	DFN1006-2	100	3.3	0.05	5	20	5	0.3	3L	Fig.1
ESD5V0L	DFN1006-2	300	5	0.5	6	15	20	160	51	Fig.2
ESD5V0LB	DFN1006-2	400	5	0.5	6	16	25	100	B8	Fig.1
ESDLC5V0LB	DFN1006-2	105	5	0.2	9	15	7	10	B2	Fig.1
ESDSC5V0L	DFN1006-2	80	5	0.1	6	16	4	0.7	-	Fig.3
ESDSC5V0LB	DFN1006-2	80	5	0.5	6	20	4	0.35	21	Fig.1
ESD7V0L	DFN1006-2	300	7	0.5	7.5	17	18	90	7P	Fig.2
ESD12VL	DFN1006-2	300	12	0.1	13.3	25	12	60	2P/12	Fig.2
ESD12VLB	DFN1006-2	350	12	0.1	13.3	25	14	50	T2	Fig.1
ESD15VL	DFN1006-2	300	15	0.5	20	30	10	65	15P	Fig.2
ESDSC16VLB	DFN1006-2	30	16	0.1	18	30	1	0.9	ZZ	Fig.1
ESD24VLB	DFN1006-2	300	24	0.1	27	40	5	20	A4	Fig.1
ESDLC5V0L3B	DFN1006-2A	60	5	0.2	6	12	5	10	-	Fig.1



## ESD Protection Devices

Part Number	Package	Peak Power Dissipation	Reverse Standoff Voltage	Maximum Reverse Leakage	Breakdown Voltage	Maximum Clamping Voltage	Maximum Peak Pulse Current	MAX Cj	Marking Code	Internal Diagram
		P <sub>PK</sub> (W)	V <sub>RWM</sub> (V)	I <sub>R</sub> (uA)	V <sub>BR</sub> (V)	V <sub>C</sub> (V)	I <sub>PP</sub> (A)	C <sub>J</sub> (pF)		
ESDLC5V0L2B	DFN1006-2L	75	5	0.5	5.6	15	5	15	H1	Fig.1
ESDLC5V0LTB	DFN1006-3	-	5	0.5	6	10	1	5	53	Fig.4
ESDLC0502P2	DFN1210-6	125	5	0.5	6	25	5	0.5	52P	Fig.6
ESDLC0502P6	DFN1610-6	125	5	0.5	6	25	5	1	02P	Fig.7
ESDLC0503P6	DFN1610-6	100	5	0.5	6	25	4	0.5	0503	Fig.8
ESDLC0504P3	DFN1616-6	60	5.5	0.5	6.5	12	5	0.4	53M	Fig.10
ESDLC3603P3	DFN1616-6	100	5.5	0.5	6.5	20	5	0.5	3654P/3603	Fig.9
ESD4V5P4	DFN2020-3L	6500	4.5	5	4.8	22	300	680	93P	Fig.35
ESD4V5P4B	DFN2020-3L	4000	4.5	1	4.8	19	210	400	42P	Fig.36
ESD7V0P4	DFN2020-3L	4000	7	1	7.5	22	180	1500	73P	Fig.35
ESD12VP4	DFN2020-3L	4000	12	1	14	30	130	450	12P	Fig.35
ESDLC3304P5	DFN2510-10	50	3.3	0.5	3.5	10	5	0.8	3324	Fig.11
ESDLC0544P5	DFN2510-10	100	6	0.5	6	25	4	0.3	0544	Fig.12
ESDLC3304P8	DFN2626-10	450	3.3	0.5	3.5	18	25	2	3304	Fig.13
ESDLC2504P9	DFN3020-10	1000	2.5	0.5	2.7	25	40	2.5	2544/2574N	Fig.14
ESDLC5V0PB8	DFN3810-9	100	5	0.5	6	20	5	0.35	0508P	Fig.15
ESDLC5V0PA6	DFN4120-10	100	5	0.5	6	20	5	0.4	0506	Fig.16
ESDLC0514MP	MSOP-10	100	5	0.5	6	20	5	0.5	514M	Fig.17
ESDH12VD1	SOD-123	4200	12	1	13	35	120	620	E12	Fig.2
ESD3V3D3	SOD-323	500	3.3	1	5	9.3	7	100	-	Fig.2
ESD3V3D3B	SOD-323	500	3.3	0.5	4.2	17	30	200	33	Fig.1
ESDLC3V3D3B	SOD-323	350	3.3	1	4	36	28	4.5	03C	Fig.18
ESDLC3V3D3	SOD-323	350	3.3	20	4	36	28	4.5	3	Fig.3
ESDSL3V3D3B	SOD-323	350	3.3	0.1	4	8	20	1.5	-	Fig.18
ESDN4V5D3B	SOD-323	2000	4.5	1	4.6	15	135	300	48D	Fig.1
ESD5V0D3B	SOD-323	120	5	1	8.8	15	8	27	B5/5.C	Fig.1
ESD5V0D3	SOD-323	350	5	1	7.3	9.8	15	350	ZA	Fig.2
ESDH5V0D3B	SOD-323	476	5	1	7.5	17	28	60	WC	Fig.1



## ESD Protection Devices

Part Number	Package	Peak Power Dissipation	Reverse Standoff Voltage	Maximum Reverse Leakage	Breakdown Voltage	Maximum Clamping Voltage	Maximum Peak Pulse Current	MAX C <sub>J</sub>	Marking Code	Internal Diagram
		P <sub>PK</sub> (W)	V <sub>RWM</sub> (V)	I <sub>R</sub> (uA)	V <sub>BR</sub> (V)	V <sub>C</sub> (V)	I <sub>PP</sub> (A)	C <sub>J</sub> (pF)		
ESDLC5V0D3B	SOD-323	350	5	1	6	32	21	5	05C/BV5	Fig.18
ESDLC5V0D3	SOD-323	350	5	5	6	32	21	5	5	Fig.18
ESDLC5V0D3B	SOD-323	350	5	0.5	6	28	18	2	R5	Fig.18
SD05	SOD-323	500	5	1	6	12.5	40	350	-	Fig.2
SD05C	SOD-323	350	5	1	6	13.5	30	125	05C	Fig.1
ESDLC8V0D3B	SOD-323	350	8	1	8.5	31.6	18	3	-	Fig.18
ESD12VD3B	SOD-323	350	12	1	13.5	15.5	1	100	12C	Fig.1
ESD12VD3	SOD-323	350	12	1	15.75	22	12	150	ZC	Fig.2
ESDLC12VD3B	SOD-323	350	12	1	13.3	28.6	11	3	2C	Fig.18
ESDN12VD3	SOD-323	990	12	1	13	27	30	100	-	Fig.2
ESDULC12VD3B	SOD-323	500	12	0.2	17.8	29	12	2	R2	Fig.18
SD15C	SOD-323	350	15	1	16.7	40	12	75	7/7	Fig.1
ESD18VD3B	SOD-323	400	18	1	20	40	10	20	-	Fig.1
ESDLC24VD3	SOD-323	330	24	1	33	44	7.5	36	-	Fig.2
ESDLC24VD3B	SOD-323	350	24	1	26.7	45	6	3	-	Fig.1
SD24C	SOD-323	350	24	1	26.7	62	8	50	8/8 or 24	Fig.1
SD36C	SOD-323	240	36	1	48	80	3	15	36C	Fig.1
ESD39VD3B	SOD-323	200	39	0.1	35.1	-	-	30	J7	Fig.1
ESDH12VD3L	SOD-323FL	350	12	1	13.3	19	15	100	E6	Fig.2
ESD3V3D5B	SOD-523	96	3.3	0.5	4.2	12	8	16	3Y	Fig.1
ESD3V3D5	SOD-523	220	3.3	0.08	5	13	16	105	ZE	Fig.2
ESDH3V0D5B	SOD-523	325	3.3	1	6	13	25	36	B3	Fig.1
ESDLC3V3D5	SOD-523	155	3.3	0.1	3.5	14.5	11	12	3D	Fig.3
ESD5V0D5	SOD-523	174	5	0.08	6.2	18.6	9.4	80	ZF	Fig.2
ESD5V0D5B	SOD-523	150	5	1	8.8	13	12	27	B5/5C	Fig.1
ESDBV5V0D5	SOD-523	52	5	1	5.6	13	4	3	HD	Fig.1
ESDH5V0D5	SOD-523	300	5	0.5	6	14	22	160	5D	Fig.2
ESDLC5V0D5B	SOD-523	150	5	0.1	7.8	12	5	12	BH	Fig.1



## ESD Protection Devices

Part Number	Package	Peak Power Dissipation	Reverse Standoff Voltage	Maximum Reverse Leakage	Breakdown Voltage	Maximum Clamping Voltage	Maximum Peak Pulse Current	MAX C <sub>J</sub>	Marking Code	Internal Diagram
		P <sub>PK</sub> (W)	V <sub>RWM</sub> (V)	I <sub>R</sub> (uA)	V <sub>BR</sub> (V)	V <sub>C</sub> (V)	I <sub>PP</sub> (A)			
ESDSL5V0D5	SOD-523	80	5	0.5	6	16	5	0.7	5Y	Fig.3
ESD7V0D5	SOD-523	200	7	0.03	7.5	22.7	8.8	65	ZH	Fig.2
ESD12VD5	SOD-523	240	12	0.02	14.1	25	9.6	55	ZM	Fig.2
ESD24VD5	SOD-523	330	24	1	33	44	7.5	36	ZY	Fig.2
ESD3V3D7	SOD-723	113	3.3	2.5	5	11.9	10.4	80	E0	Fig.2
ESD5V0D7	SOD-723	117	5	1	6.2	13.3	8.8	65	E2	Fig.2
ESDLC5V0D7B	SOD-723	100	5	0.1	5.8	12.5	5	12	H	Fig.1
ESD12VD7	SOD-723	128	12	1	13.5	23.7	5.4	30	E3	Fig.2
ESD5V0LP	SOD-882	107	5	1	6.2	12.3	8.7	65	B	Fig.2
ESD5V0LPB	SOD-882	100	5	0.1	8.8	12.5	11.2	30	EB	Fig.1
ESDLC5V0LPB	SOD-882	100	5	0.1	8	12.5	5	12	H	Fig.1
ESDSL5V0LP	SOD-882	105	5	1	5.4	10	1	0.9	-	Fig.2
ESDSL5V0LPB	SOD-882	100	5	1	8	13	4	3	HB	Fig.1
ESDSL3V3LPBE	SOD-882E	150	3.3	1	4.8	10	1	0.9	-	Fig.1
ESD3V3D9	SOD-923	100	3.3	1	5	8.8	5	50	E1	Fig.2
ESD3V3D9B	SOD-923	80	3.3	1	4	-	-	8	-	Fig.1
ESD5V0D9	SOD-923	100	5	1	7.2	11	5	40	B	Fig.2
ESD5V0D9B	SOD-923	80	5	1	7.8	11	5	15	9C/C	Fig.1
ESDLC5V0D9	SOD-923	100	5	1	8.5	9.8	1	0.5	9L	Fig.2
ESDLC5V0D9B	SOD-923	80	5	1	7.8	9	1	2.1	1L	Fig.1
ESD7V0D9	SOD-923	100	7	1	8	14.8	5	30	9M	Fig.2
ESD12VD9	SOD-923	100	12	1	13.5	18.4	1	15	C	Fig.2
ESD2V8P8U	SOP-8	400	2.8	1	3	8.5	5	3	SLVU2.8-4	Fig.19
ESDLC2V8P8U	SOP-8	400	2.8	1	3	8	5	0.7	-	Fig.19
SLVU2.8-4	SOP-8	600	2.8	1	3	15	24	1	2.8-4	Fig.19
LC03-3.3	SOP-8	7200	3.3	0.5	3.5	48	150	16	LC-3.3	Fig.20
ESD5V0T143-4U	SOT-143	500	5	5	6	20	28	3	R05A	Fig.21
LSR05	SOT-143	90	5	0.5	6	18	5	0.4	R0502	Fig.21



## ESD Protection Devices

Part Number	Package	Peak Power Dissipation	Reverse Standoff Voltage	Maximum Reverse Leakage	Breakdown Voltage	Maximum Clamping Voltage	Maximum Peak Pulse Current	MAX Cj	Marking Code	Internal Diagram
		P <sub>PK</sub> (W)	V <sub>RWM</sub> (V)	I <sub>R</sub> (uA)	V <sub>BR</sub> (V)	V <sub>C</sub> (V)	I <sub>PP</sub> (A)	C <sub>J</sub> (pF)		
SR05	SOT-143	500	5	0.5	6	20	25	6	ASR05	Fig.21
SR70	SOT-143	-	70	5	85	7	24	10	SR70	Fig.22
SLVU2.8	SOT-23	500	2.8	1	3	8	5	13	U2.8	Fig.27
ESD3V3AP	SOT-23	300	3.3	10	5	7.5	13.3	150	3M3	Fig.23
SM3.3	SOT-23	360	3.3	1.5	6	20	18	200	S33 M	Fig.23
SM3.3B	SOT-23	350	3.3	1.5	4	10	25	100	33C	Fig.26
ESDLC5V0C2	SOT-23	650	5	1	6	26	25	2	V5C1	Fig.24
ESDSL5V0T2	SOT-23	100	5	0.5	6	20	5	0.4	5M	Fig.23
ESDU5V0C2	SOT-23	87.5	5	1	6	25	3.5	0.8	U5C2	Fig.25
LSM05B	SOT-23	50	5	0.1	8	10	5	15	BL5A	Fig.26
SM05	SOT-23	300	5	10	7.3	9.8	12	110	5M	Fig.23
ESDA6V1L	SOT-23	300	5.25	20	7.2	9	1	140	EL61	Fig.23
SL12	SOT-23	300	12	0.5	13.3	24	12	5	L12	Fig.23
SM12	SOT-23	300	12	1	15.75	19	11.2	60	12M/12C	Fig.23
SM12B	SOT-23	300	12	0.5	13.3	30	10	32	-	Fig.26
SM12C	SOT-23	300	12	1	15.75	19	11.2	95	12L	Fig.28
SM712	SOT-23	400	12	1	13.3	21	5	45	B712/712	Fig.26
SM15B	SOT-23	300	15	0.2	17	35	8	25	15C	Fig.26
SM22	SOT-23	300	22	1	27	44	3	80	S24	Fig.23
SM24B	SOT-23	300	24	0.2	27	50	4	20	24C/24M	Fig.26
SM36	SOT-23	240	36	1	48	80	3	30	36M	Fig.23
SM36B	SOT-23	300	36	0.2	38	90	3	15	36C	Fig.26
ESDA6V1-4L	SOT23-6L	80	5	1	6.1	12	3	10	VT5	Fig.30
ESDU5V0H4	SOT23-6L	125	5	1	8.8	25	5	0.8	U5H4	Fig.31
PSRV05-4	SOT23-6L	500	5	1	6	20	25	4	V05	Fig.31
SRV05-4A	SOT23-6L	300	5	5	6	25	12	3	V05/SV05	Fig.31
SRV05-4L	SOT23-6L	350	5	1	6	17.5	12	1.5	V05	Fig.31
ESD12VK4	SOT23-6L	220	12	1	13.3	24	9	45	12VK4	Fig.29



## ESD Protection Devices

Part Number	Package	Peak Power Dissipation	Reverse Standoff Voltage	Maximum Reverse Leakage	Breakdown Voltage	Maximum Clamping Voltage	Maximum Peak Pulse Current	MAX C <sub>J</sub>	Marking Code	Internal Diagram
		P <sub>PK</sub> (W)	V <sub>RWM</sub> (V)	I <sub>R</sub> (uA)	V <sub>BR</sub> (V)	V <sub>C</sub> (V)	I <sub>PP</sub> (A)	C <sub>J</sub> (pF)		
ESD5V0J4	SOT-353	200	5	2	7.2	12.5	5	90	12	Fig.32
ESDLC5V0J4	SOT-353	20	5	1	7.2	9.8	1.6	14.5	4H	Fig.32
ESD5V0K5	SOT-363	100	5	5	7.2	12.5	8	130	22	Fig.33
ESDLC5V0K5	SOT-363	100	5	1	7.2	10.8	1.6	11.5	L2	Fig.33
SRV05-4	SOT-363	-	5	2	6	15	1	2	-	Fig.31
ESDLC5V0T5	SOT-523	125	5	1	5.6	23	5	2	P5	Fig.23
ESDU5V0T5	SOT-523	56	5	1	9.4	11	4	0.5	U5E2/P5	Fig.23
ESDLC3V0L4	SOT-553	20	3	1	5.9	8	2.5	9	5P	Fig.32
ESD5V0L4	SOT-553	200	5	5	7.2	13.5	5	80	42	Fig.32
ESDLC5V0L4	SOT-553	20	5	1	7.2	11	1.6	14	5H	Fig.32
ESD5V0M5	SOT-563	100	5	5	7.2	13.5	5	32	52	Fig.33
ESDLC5V0M5	SOT-563	20	5	1	7.2	12	1.6	10	L2	Fig.33
ESDU5V0M5	SOT-563	90	5	1	10	25	3.5	0.8	U5N2	Fig.34
SRV05-4S	SOT-563	125	5	1	6	25	5	0.8	-	Fig.31
ESDLC5V0T7	SOT-723	110	5	1	5.6	23	5.5	2	T5	Fig.23
ESDLC5V0BM	SOT-883	50	5	0.1	8	10	5	10	H	Fig.26



## ESD Protection Devices

### Internal Diagram

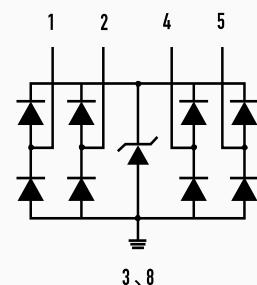
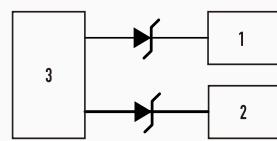


Fig.1

Fig.2

Fig.3

Fig.4

Fig.5

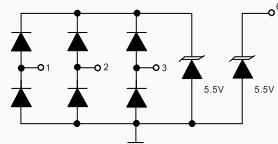
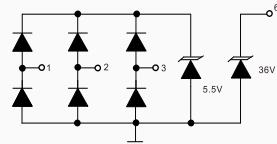
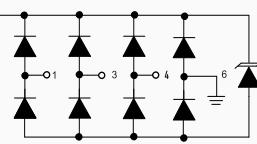
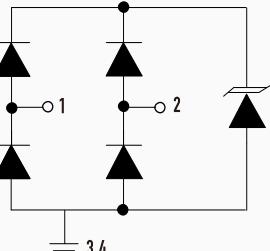
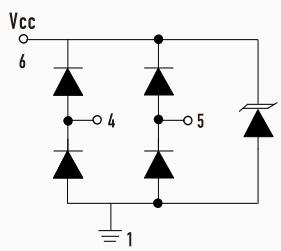


Fig.6

Fig.7

Fig.8

Fig.9

Fig.10

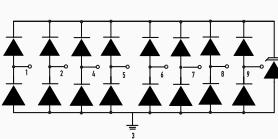
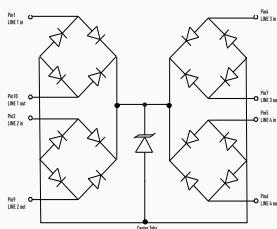
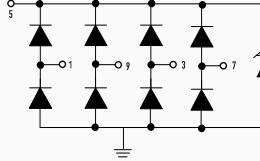
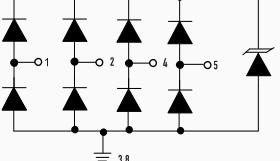
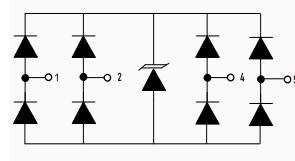


Fig.11

Fig.12

Fig.13

Fig.14

Fig.15

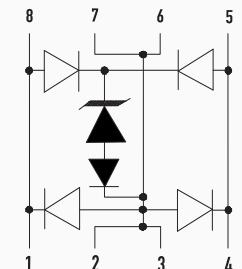
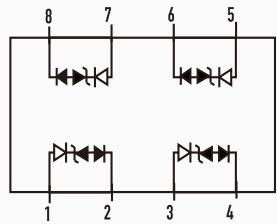
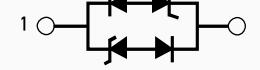
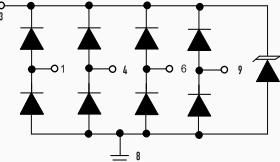
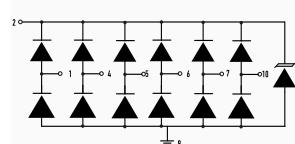


Fig.16

Fig.17

Fig.18

Fig.19

Fig.20

## ESD Protection Devices

### Internal Diagram

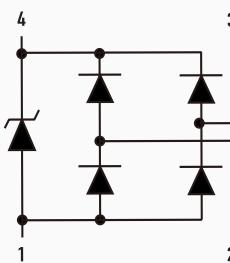


Fig.21

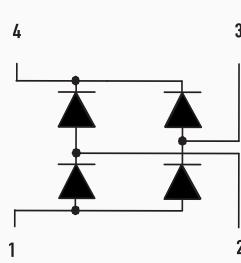


Fig.22

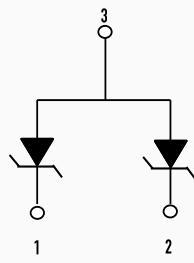


Fig.23

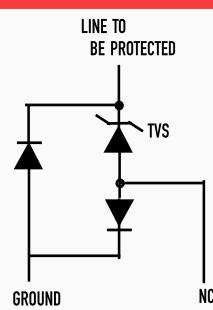


Fig.24

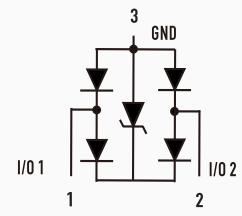


Fig.25

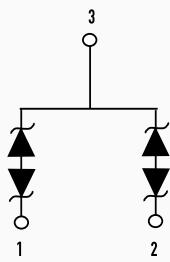


Fig.26

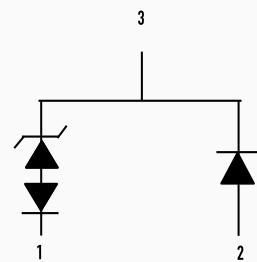


Fig.27

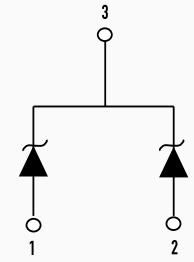


Fig.28

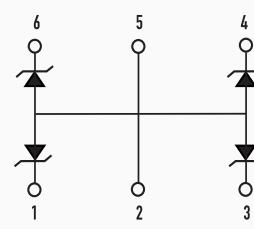


Fig.29

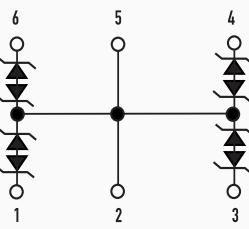


Fig.30

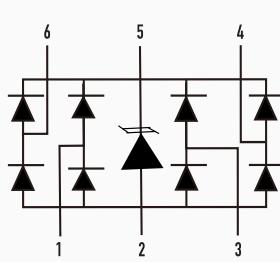


Fig.31

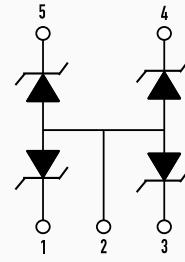


Fig.32

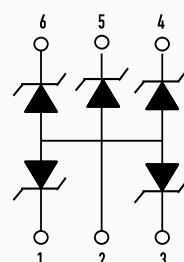


Fig.33

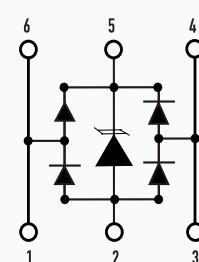


Fig.34



Fig.35

Fig.36