



Micro Commercial Components

Features

- Lead Free Finish/RoHS Compliant(Note 1) ("P" Suffix designates
- # # RoHS Compliant. See ordering information)
- Zener Voltage 3.3V to 12V and Low reverse leakage current
- Silicon Planar Power Zener Diodes
- Standards zener voltage tolerance is ±5%, other tolerances are available upon request

Mechanical Data

- Case:Minimelf Package
- Polarity: Color band denotes cathode end
- Weight: Approx. 0.05gram
- Moisture Sensitivity Level 1

Maximum Ratings

	Symbol	Value	Units
Zener Current		See Table 1	
Power Dissipation $@T_A=50^{\circ}C$	P _{tot}	500	mW
Junction Temperature	TJ	200	S
Storage Temperature Range	T _{STG}	-65 to 200	°C

Electrical Characteristics $@25^\circ$ C Unless Otherwise Specified

	Symbol	Maximum	Unit
Thermal resistance	$R_{ heta JA}$	300	°C/W
Forward Voltage	V	1.5	V
@ I _F =200mA	V _F	1.5	v

NOTE: 1) Lead in Glass Exemption Applied, see EU Directive Annex 7(C)-I.

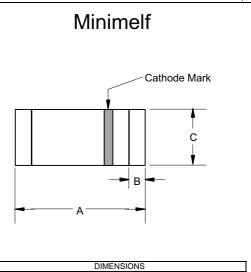
- 2) Valid provided that a distance of 8mm from case are kept at ambient temperature
- 3) Power derating: 4.0mW/°C above 50°C



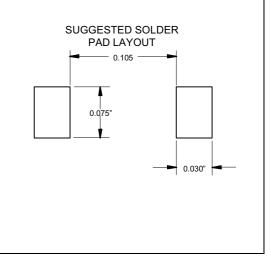
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DL746A THRU DL759A

0.5W Silicon Planar Zener Diodes



DIMENSIONS					
	INCHES		MM		
DIM	MIN	MAX	MIN	MAX	NOTE
Α	.130	.146	3.30	3.70	
В	.008	.016	.20	.40	
С	.055	.059	1.40	1.50	Ø



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DL746A thru DL759A



	NORMAL ZENER	TEST	MAXIMUM ZENER	MAXIMUM	REVERSE	MAXIMUM ZENER	
MCC PART NUMBER	VOLTAGE	CURRENT	IMPEDANCE	LEAKAGE	CURRENT	CURRENT	TYPICAL TEMP.
NUNDER	Vz@ Izt	Izt	Zzt @ Izt	lr @	Vr=1V	L	COEFFICIENT
	VOLTS	mA	OHMS	µA @25⁰C	µA @125ºC	mA	%/°C
DL746A	3.3	20	28	10	30	110	066
DL747A	3.6	20	24	10	30	100	058
DL748A	3.9	20	23	10	30	95	046
DL749A	4.3	20	22	2	30	85	033
DL750A	4.7	20	19	2	30	75	015
DL751A	5.1	20	17	1	20	70	±.010
DL752A	5.6	20	11	1	20	65	+.030
DL753A	6.2	20	7.0	0.1	20	60	+.049
DL754A	6.8	20	5.0	0.1	20	55	+.053
DL755A	7.5	20	6.0	0.1	20	50	+.057
DL756A	8.2	20	8.0	0.1	20	45	+.060
DL757A	9.1	20	10	0.1	20	40	+.061
DL758A	10	20	17	0.1	20	35	+.062
DL759A	12	20	30	0.1	20	30	+.062

Note:

- 1) Tested with pulses $t_p = 20ms$
- 2) Valid provided that leads are kept at ambient temperature at a distance of 8mm from case.
- 3) Zener impedance derived by superimposing on $I_{\rm ZT}$, a 60 cps, rms ac current equal to 10% $I_{\rm ZT}$ (2 mA ac)
- 4) Allowance has been made for the increase in V_z due to Z_z and for the increase in junction temperature as the unit approaches thermal equilibrium at the power dissipation of 400mW.

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Ordering Information :

Device	Packing		
Part Number-TP	Tape&Reel: 2.5Kpcs/Reel		

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