



130 W Cochran St, Unit B  
Simi Valley, CA 93065  
Tel:818-701-4933

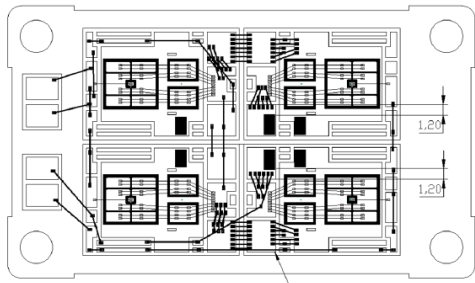
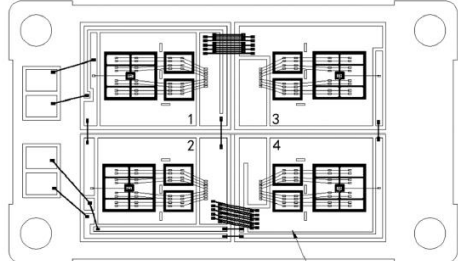
## Process Change Notification (PCN)

<b>Notification number:</b>	<b>101623-1</b>
<b>Notification date:</b>	<b>16 Oct 2023</b>
<b>Proposed implementation date:</b>	<b>16 Jan 2024</b> (90 days notification before change, according to JEDEC standard)
<b>Product type affected:</b>	Please refer to table 1 in Appendix 1.
<b>Change Category</b>	Updated C2 package DBC and AI wire layout
<b>Change Classification</b>	Major
<b>Change description:</b>	Please refer to table 2 in Appendix 1.
<b>Reason for change:</b>	Optimize DBC layout design to improve the product quality and production efficiency.
<b>Deposition of old product</b>	No inventory now
<b>Identification of Changed product:</b>	No changes to product marking
<b>Contact person:</b>	Please contact your respective Account manager (AM) / Inside sales representative (ISR/CSR) if you have further query.
<b>Approved by:</b>	Seaman Wu (Director of Engineering) Steve Zhang (Director of Supply Chain) Woods Zhang (Director of Quality) Pamela Cheng (GM & EVP of Sales)

Appendix 1: Table 1

No.	Product type	Existing marking	New marking	Package type/code
1	MIF150R12C2TL-BP	MIF150R12C2TL	MIF150R12C2TL	C2
2	MIF200R12C2TL-BP	MIF200R12C2TL	MIF200R12C2TL	C2
3	MIF300R12C2TL-BP	MIF300R12C2TL	MIF300R12C2TL	C2
4	MIF450R12C2TL-BP	MIF450R12C2TL	MIF450R12C2TL	C2
5	MIF200R12C2TF-BP	MIF200R12C2TF	MIF200R12C2TF	C2

Appendix 1: Table 2

<b>C2 Package DBC and AI wire Layout Comparison</b>		
Item	Current	New
DBC Layout		
Remark	Complex layout	Simplified layout

Note: Compared with current and new product, there is no obvious difference in electrical parameters of datasheet.



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**Appendix 2: Reliability Report**

**Part Number: MIF300R12C2TL-BP**

**Date: 2023-3-28**

**Test Results: PASS**

Test Item	Conditions	Duration	Quantity	Rejects
<b>TEST</b> Pre- and Post-Stress Electrical Test	T <sub>a</sub> = 25 °C	N/A	all parts	see below
<b>HTRB</b> High Temperature Reverse Bias	MIL-STD-750 Method 1038 T <sub>j</sub> =T <sub>jmax</sub> ,80%VR	1000 hours	5Pcs	0
<b>HTGB</b> High Temperature Gate Bias	JESD22-A-108 T <sub>j</sub> =T <sub>jmax</sub> ,100%Vgs	1000 hours	5Pcs	0
<b>H3TRB</b> High Humidity High Temperature Reverse Bias	IEC60749-5 85±2 °C , 85±5%RH,80% VDS(Max=100V)	1000 hours	5Pcs	0
<b>TC</b> Temperature Cycling	JESD22-A104 -55 °C (+0,-10)/15Min~ 150(+15,-0)/15Min,	100Cycles	5Pcs	0
<b>PC</b> Power Cycles	ΔT <sub>j</sub> =100 °C, T <sub>jmin</sub> =25°C ton + toff <=6s	20000 cycles	5Pcs	0
<b>LTSL</b> Low Temperature Storage Life	JESD22-A119 Tstgmin	1000 hours	5Pcs	0
<b>HTSL</b> High Temperature Storage Life	JESD22-A103 Tstgmax	1000 hours	5Pcs	0



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Date: 16 Oct 2023

PCN #: 101623-1

PCN Title: Updated C2 package DBC and AI wire layout

Dear Customer.

This is a PCN announcement to the above-mentioned product which is/are offered by Micro Commercial Components Corp (MCC). We would appreciate your acknowledgement of receipt of this notification within 30 days of the date of this PCN to your local ISR, sales representative. Please refer to the attached document for more information (including implementation date / product date code of this change). If you have any questions or concerns related to this PCN, please contact your local sales representative / ISR for support.

Yours sincerely,

PCN Team