

Features

- Trench MV MOSFET Technology
- · Moisture Sensitivity Level 1
- Halogen Free. "Green" Device (Note1)
- Epoxy Meets UL 94 V-0 Flammability Rating
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

N-CHANNEL MOSFET

Maximum Ratings

- Operating Junction Temperature Range: -55°C to +150°C
- Storage Temperature: -55°C to +150°C
- Thermal Resistance: 370°C/W Junction to Ambient^(Note2)

Parameter		Symbol	Rating	Unit	
Drain-Source Voltage		V _{DS}	100	V	
Gate-Source Volltage		V _{GS}	±20	V	
Continuous Drain Current	T _A =25°C	- I _D	200	A	
	T _A =100°C		126	mA	
Pulsed Drain Current ^(Note3)		I _{DM}	800	mA	
Total Power Dissipation (Note4)		P _D	337	mW	

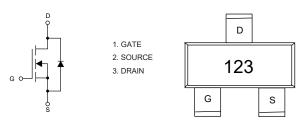
Note:

- 1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 2. The value of $R_{\theta JA}$ is measured with the device mounted on 1in2 FR-4 board with 2oz. Copper, in a still air environment with T_A =25°C.
- 3. Repetitive rating; pulse width limited by max. junction temperature.
- 4. P_D is based on max. junction temperature, using junction-ambient thermal resistance.

SOT-723

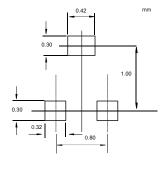
DIMENSIONS						
DIM INCHES		MM		NOTE		
DIIVI	MIN		MIN MAX		INOIL	
Α	0.043	0.051	1.10	1.30		
В	0.043	0.051	1.10	1.30		
С	0.028	0.035	0.70	0.90		
D	0.031		0.80		TYP.	
E	0.009	0.017	0.22	0.42		
F	0.005	0.013	0.12	0.32		
G	0.000	0.002	0.00	0.05		
Н	0.017	0.021	0.43	0.54		
J	0.003	0.006	0.08	0.15		

Suggested Solder Pad Laves



Internal Structure and Marking Code

Suggested Solder Pad Layout





Electrical Characteristics @ 25°C (Unless Otherwise Specified)

Parameter	Symbol	Test conditions	Min	Тур	Max	Unit	
Static Characteristics							
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, I _D =250μA	100			V	
Gate-Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250uA	1.0	1.8	2.5	V	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =100V, V _{GS} =0V			1	μA	
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V, V _{DS} =0V			±100	nA	
Drain-Source On-Resistance	В	V _{GS} =10V, I _D =0.2A		2.4 3.4			
Dialii-Source Off-Nesistance	R _{DS(on)}	V _{GS} =4.5V, I _D =0.2A		2.65	3.6	Ω	
Forward Transconductance	9 _{FS}	V _{DS} =5V, I _D =0.17A		450		mS	
Gate Resistance	R_g	f=1 MHz, Open drain		5.5		Ω	
Diode Characteristics						I.	
Continuous Body Diode Current	Is				0.2	Α	
Diode Forward Voltage	V _{SD}	V _{GS} =0V, I _S =0.2A			1.2	V	
Reverse Recovery Time	t _{rr}	1 - 1		20		ns	
Reverse Recovery Charge	Q _{rr}	I _F =1A, dI _F /dt=100A/μs		6		nC	
Dynamic Characteristics							
Input Capacitance	C _{iss}			35			
Output Capacitance	C _{oss}	V_{DS} =50V, V_{GS} =0V,f=1MHz		2		pF	
Reverse Transfer Capacitance	C _{rss}			1.3			
Total Gate Charge	Q_g			1.8			
Gate-Source Charge	Q _{gs}	V _{DS} =50V,V _{GS} =10V,I _D =1A		0.6		nC	
Gate-Drain Charge	Q_{gd}			0.3			
Turn-On Delay Time	t _{d(on)}			4			
Turn-On Rise Time	t _r	V _{DD} =50V,V _{GS} =10V,		20			
Turn-Off Delay Time	t _{d(off)}	$R_G=3\Omega$, ID=1A		7		ns	
Turn-Off Fall Time	t _f			31			



Curve Characteristics

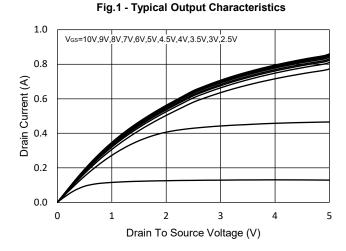


Fig.2 - Transfer Characteristics

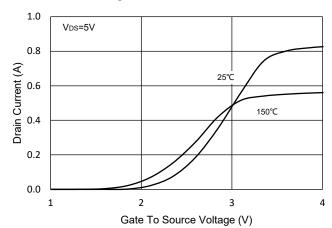


Fig.3 - $R_{\rm DS(ON)}$ - $V_{\rm GS}$

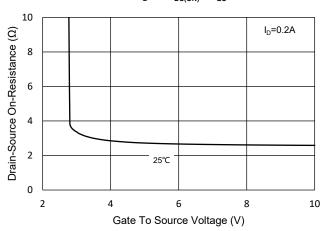


Fig.4 - $R_{DS(ON)}$ - I_D

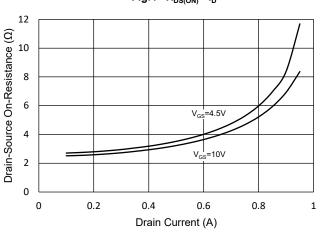


Fig.5 - Capacitance Characteristics

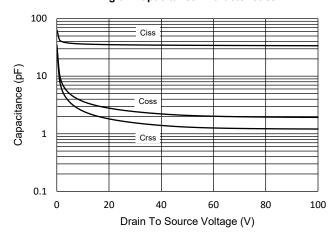
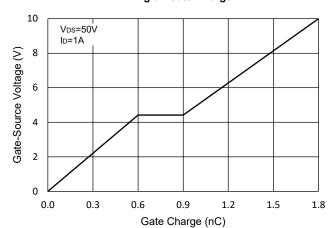
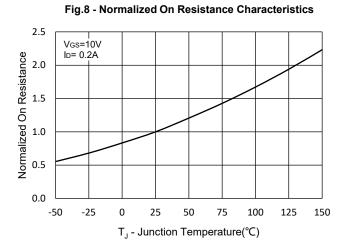


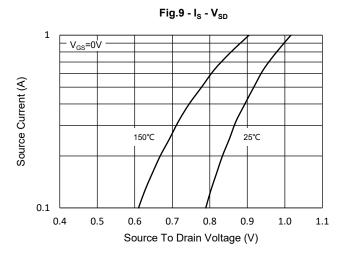
Fig.6 - Gate Charge

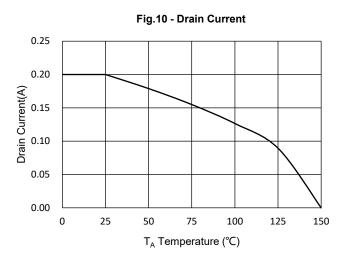


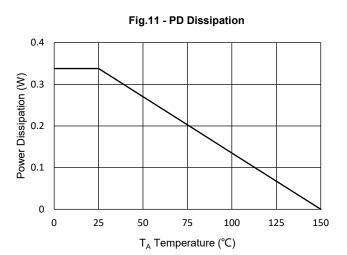


Curve Characteristics



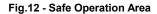








Curve Characteristics



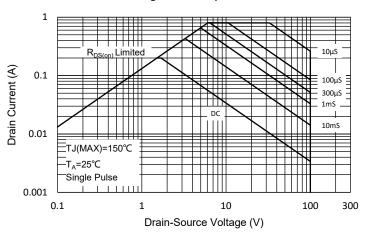
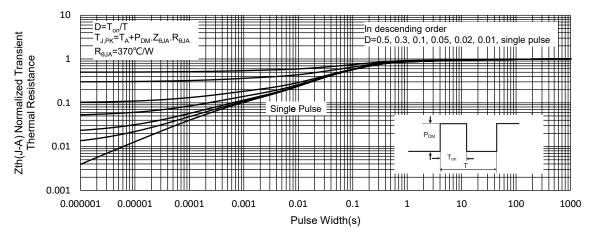


Fig.13 - Normalized Transient Thermal Impedance





Ordering Information

Device	Packing
Part Number-TP	Tape&Reel: 8Kpcs/Reel

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