

Features

- AEC-Q101 Qualified
- Trench Power LV MOSFET Technology
- Moisture Sensitivity Level 1
- Halogen Free. "Green" Device (Note 1)
- Epoxy Meets UL 94 V-0 Flammability Rating
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

Maximum Ratings

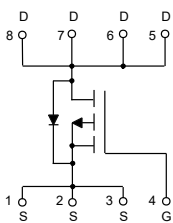
- Operating Junction Temperature Range : -55°C to +150°C
- Storage Temperature Range: -55°C to +150°C
- Thermal Resistance: 50°C/W Junction to Ambient^(Note 2)
- Thermal Resistance: 3°C/W Junction to Case

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	-40	V
Gate-Source Voltage	V_{GS}	±20	V
Continuous Drain Current	I_D	$T_C=25^\circ\text{C}$	-45
		$T_C=100^\circ\text{C}$	-28
Pulsed Drain Current ^(Note 3)	I_{DM}	-180	A
Total Power Dissipation ^(Note 4)	P_D	41.7	W
Single Pulsed Avalanche Energy ^(Note 5)	E_{AS}	132	mJ

Note:

1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
2. The value of $R_{\theta JA}$ is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with $T_A=25^\circ\text{C}$.
3. Pulse Test: Pulse Width≤300us, Duty cycle ≤2%.
4. P_d is based on max. junction temperature, using junction-case thermal resistance.
5. $T_j=25^\circ\text{C}$, $V_{DD}=-30\text{V}$, $R_G=25\Omega$, $V_G=-10\text{V}$, $L=0.5\text{mH}$.

Internal Structure and Marking Code

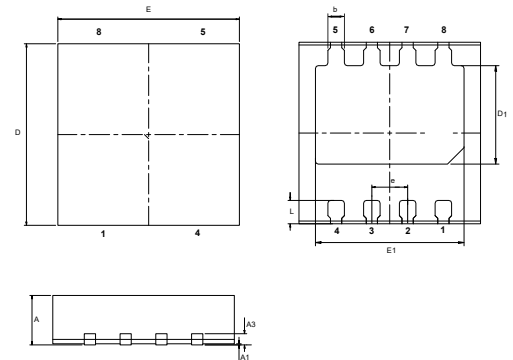


pin1

YYWW: 4 codes in total
YY is the year
WW is the week

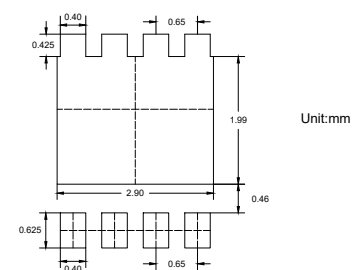
P-CHANNEL MOSFET

DFN3333-8(SWF)



DIM	INCHES		MM		NOTE
	MIN	MAX	MIN	MAX	
A	0.028	0.031	0.70	0.80	
A1	0.000	0.002	0.00	0.05	
A3	0.008		0.20		TYP.
b	0.010	0.014	0.25	0.35	
D	0.130		3.30		TYP.
E	0.130		3.30		TYP.
e	0.026		0.65		TYP.
D1	0.066	0.074	1.69	1.89	
E1	0.102	0.110	2.60	2.80	
L	0.013	0.021	0.325	0.525	

Suggested Solder Pad Layout



Electrical Characteristics @ 25°C (Unless Otherwise Specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS}=0V, I_D=-250\mu A$	-40			V
Gate-Source Leakage Current	I_{GSS}	$V_{DS}=0V, V_{GS}=\pm 20V$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=-40V, V_{GS}=0V$			-1	μA
Gate-Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=-250\mu A$	-1	-1.6	-3	V
Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS}=-10V, I_D=-10A$		10	13	m Ω
		$V_{GS}=-4.5V, I_D=-8A$		13	17	
Gate Resistance	R_g	f=1MHz, Open drain		9		Ω
Diode Characteristics						
Continuous Body Diode Current	I_S				-45	A
Diode Forward Voltage	V_{SD}	$V_{GS}=0V, I_S=-20A$			-1.2	V
Reverse Recovery Time	t_{rr}	$I_S=-25A, di_F/dt=100A/\mu s$		92.5		ns
Reverse Recovery Charge	Q_{rr}			25		nC
Dynamic Characteristics						
Input Capacitance	C_{iss}	$V_{DS}=-25V, V_{GS}=0V, f=1MHz$		3136		pF
Output Capacitance	C_{oss}			247		
Reverse Transfer Capacitance	C_{rss}			225		
Total Gate Charge	Q_g	$V_{DS}=-20V, V_{GS}=-10V, I_D=-25A$		73.4		nC
Gate-Source Charge	Q_{gs}			9		
Gate-Drain Charge	Q_{gd}			15.3		
Turn-On Delay Time	$t_{d(on)}$	$V_{DD}=-20V, V_{GS}=-10V, R_G=6\Omega, I_{DS}=-25A$		13.7		ns
Turn-On Rise Time	t_r			12		
Turn-Off Delay Time	$t_{d(off)}$			201		
Turn-Off Fall Time	t_f			92.5		

Curve Characteristics

Fig. 1 - Typical Output Characteristics

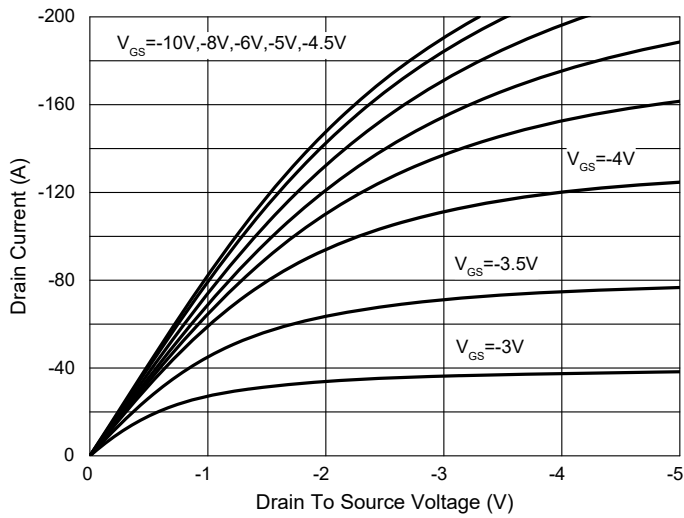


Fig. 2 - Transfer Characteristics

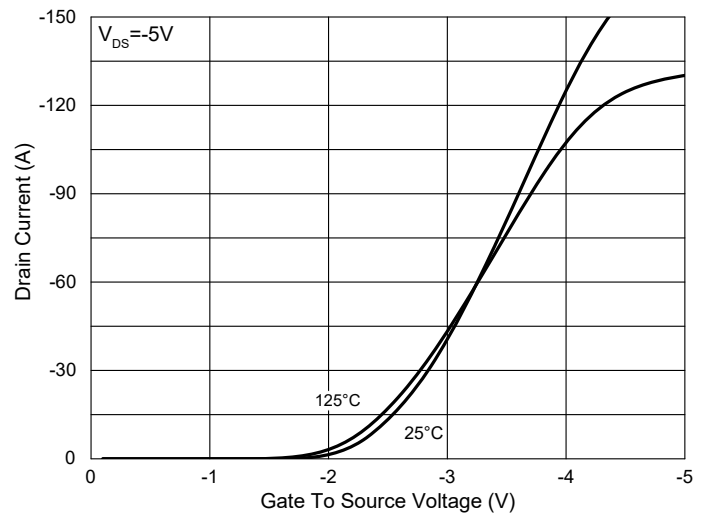


Fig. 3 - $R_{DS(ON)} - V_{GS}$

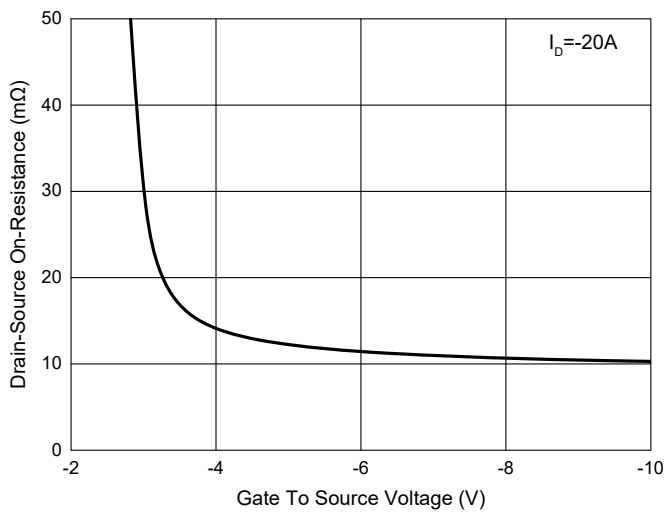


Fig. 4 - $R_{DS(ON)} - I_D$

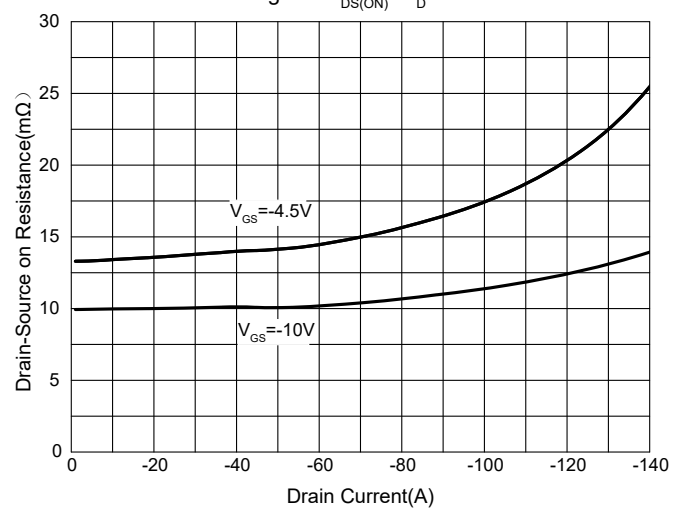


Fig. 5 - Capacitance Characteristics

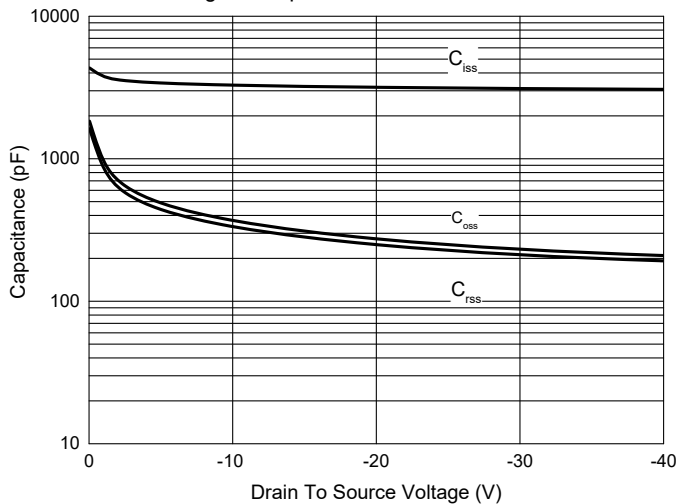
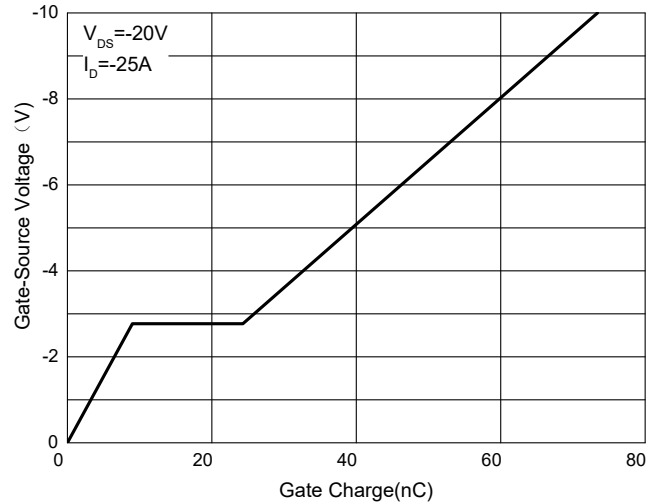


Fig. 6 - Gate Charge



Curve Characteristics

Fig. 7 - Normalized Threshold Voltage

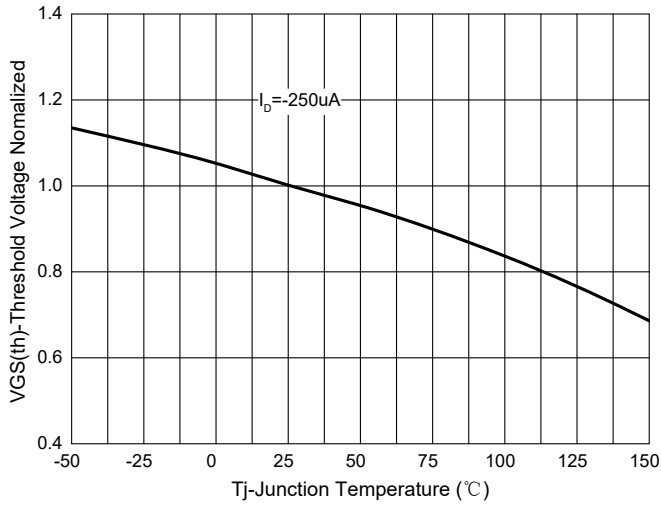


Fig.8-Normalized On Resistance Characteristics

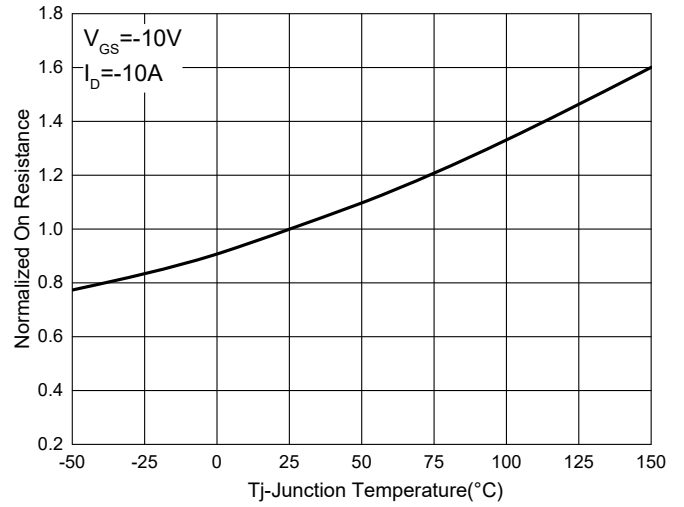


Fig.9 - $I_s - V_{SD}$

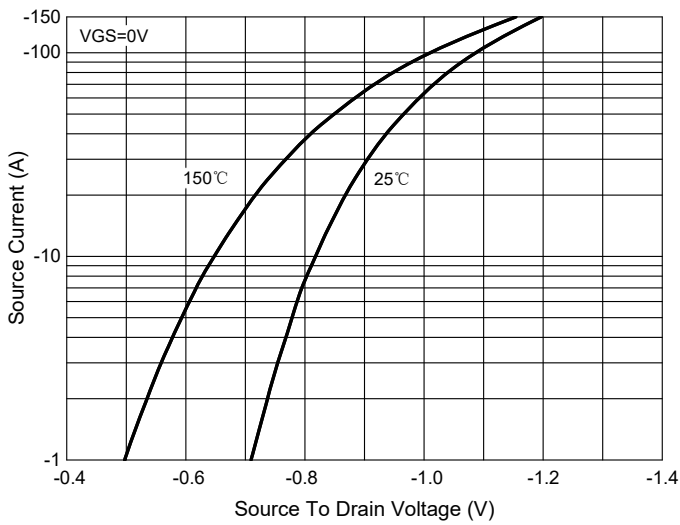


Fig. 10 - Drain Current

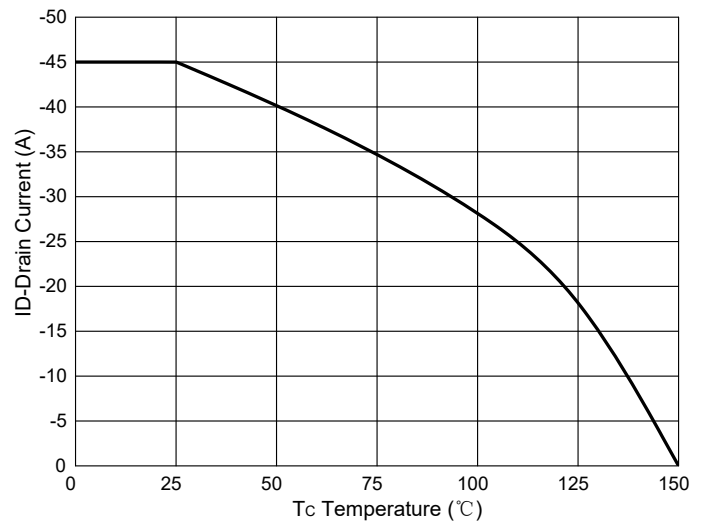
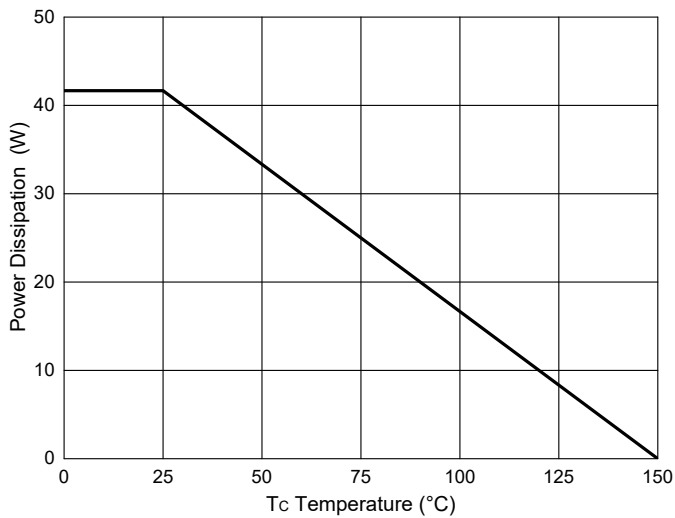


Fig.11-PD Dissipation



Curve Characteristics

Fig. 12 - Safe Operation Area

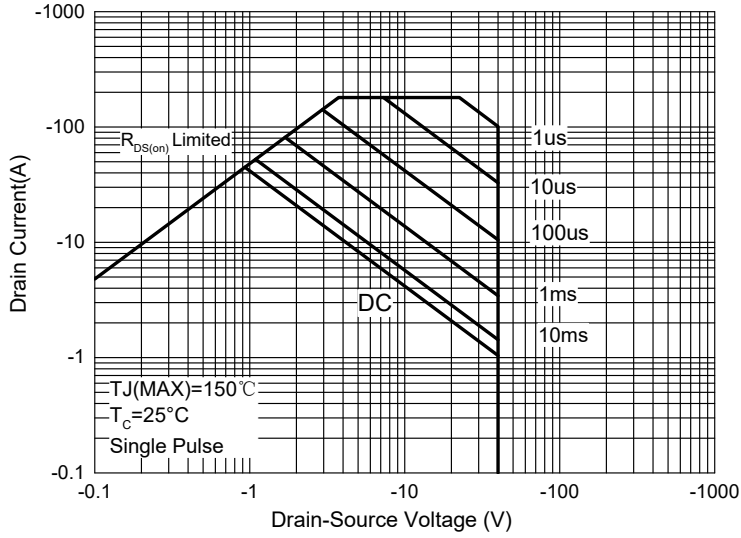
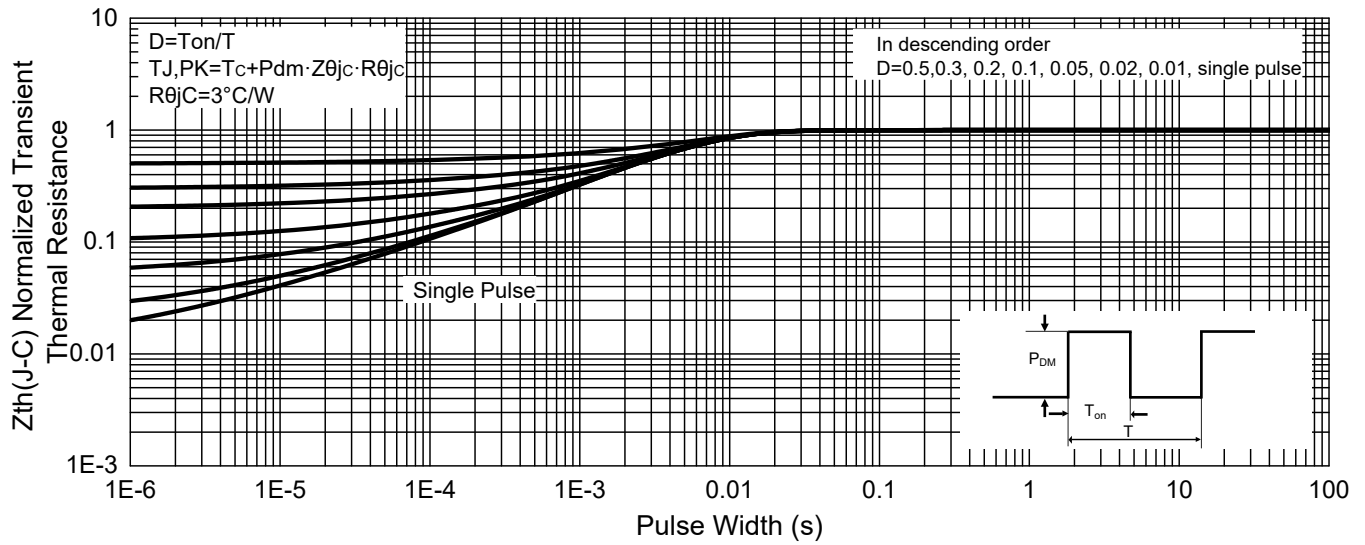


Fig. 13 - Normalized Transient Thermal Impedance



Ordering Information

Device	Packing
Part Number-TP	Tape&Reel: 5Kpcs/Reel

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